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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/784,417
	Filing Date	February 23, 2004
	First Named Inventor	Minchang Liang
	Art Unit	2822
	Examiner Name	Ida M. Soward
Total Number of Pages In This Submission	Attorney Docket Number	A1385

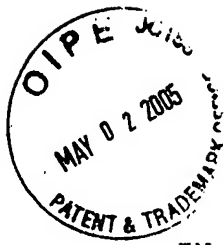
ENCLOSURES (Check all that apply)		
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Remarks The Commissioner is hereby authorized to charge any fee due in connection with this submission to Deposit Account No. 502942.		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	G. Victor Treyz
Signature	<i>G. Victor Treyz</i>
Date	4/28/05

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A1385

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Minchang Liang et al.  
Application No. : 10/784,417  
Confirmation No. : 1815  
Filed : February 23, 2004  
For : BIPOLAR TRANSISTORS WITH LOW BASE  
RESISTANCE FOR CMOS INTEGRATED CIRCUITS  
Group Art Unit : 2822  
Examiner : Ida M. Soward  
Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the restriction requirement of April 5, 2005 in connection with the above-identified patent application, applicants hereby elect without traverse the invention of group I (claims 1-5 and 18-20) for further prosecution.

The Commissioner is hereby authorized to charge any

fee due in connection with this submission to deposit account  
No. 502942.

Respectfully submitted,

 4/28/05

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